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APPLICANTS

Junichiro Noda, Kanagawa-ken, JAPAN;

Tamio Ikehashi, Kanagawa-ken, JAPAN;
Kenichi Imamiya, Tokyo, JAPAN;** CONTINUING DATA ***** *YES*This application is a DIV of 10/270,673 10/16/2002 PAT 6,731,538
which is a CIP of 09/794,076 02/28/2001 ABN** FOREIGN APPLICATIONS ***** *NO*

JAPAN 2000-066954 03/10/2000

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 04/09/2004

Foreign Priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY	SHEETS	TOTAL CLAIMS	INDEPENDENT CLAIMS
35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after				
Verified and Acknowledged	<i>Phasorony JP</i> Examiner's Signature Initials	JAPAN	DRAWING 20	8	2

ADDRESS

22850
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TITLE

Semiconductor memory device including page latch circuit

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue)
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